

References

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2. Zwanziger, Peter; US Patent 4,987,362, "*Self-regulating drive circuit for the base current of a power transistor with saturation level control*", Issued: January 22, 1991
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4. Alwardi, Milad et. al.; US Patent 5,965,997, "*Battery monitoring circuit with storage of charge and discharge accumulation values accessible therefrom*", Issued: October 12, 1999
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7. Technology and product information for JFET devices from Lovoltech Inc. at the following URL: <http://www.lovoltech.com/products.htm>

Diagrams

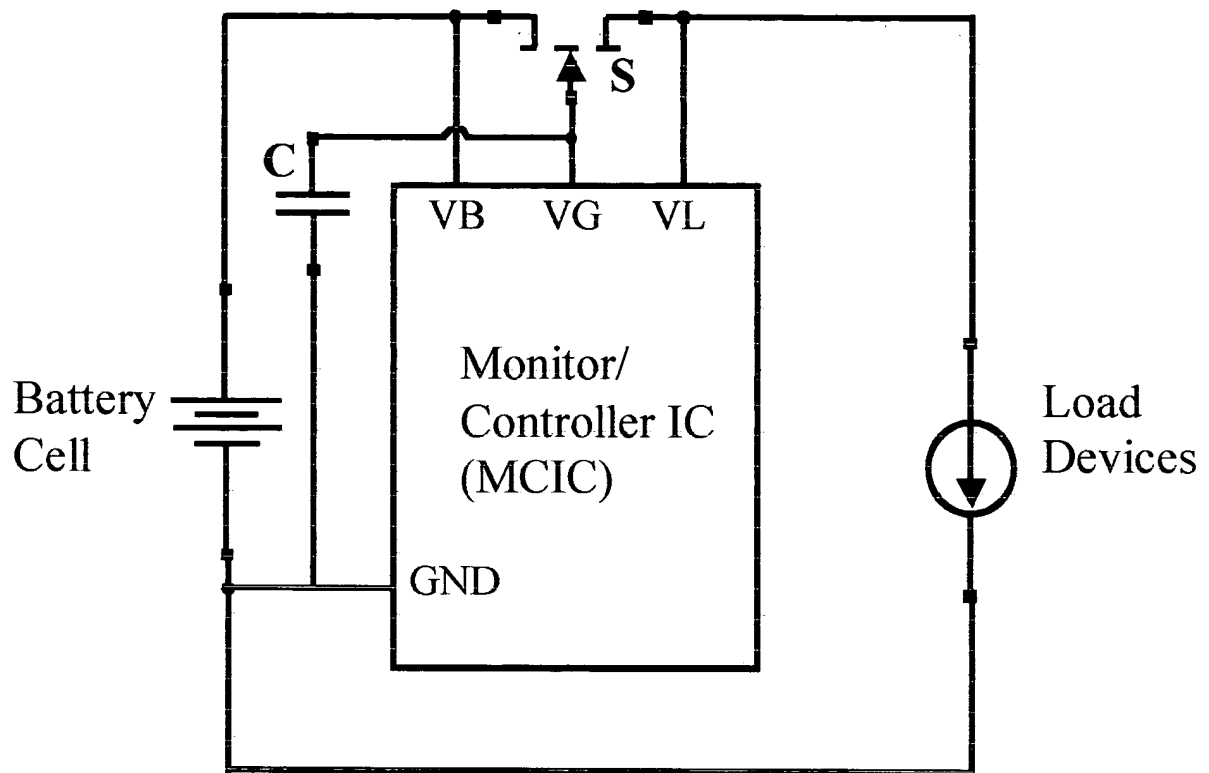


Fig. 1: One possible system-level embodiment of the invention

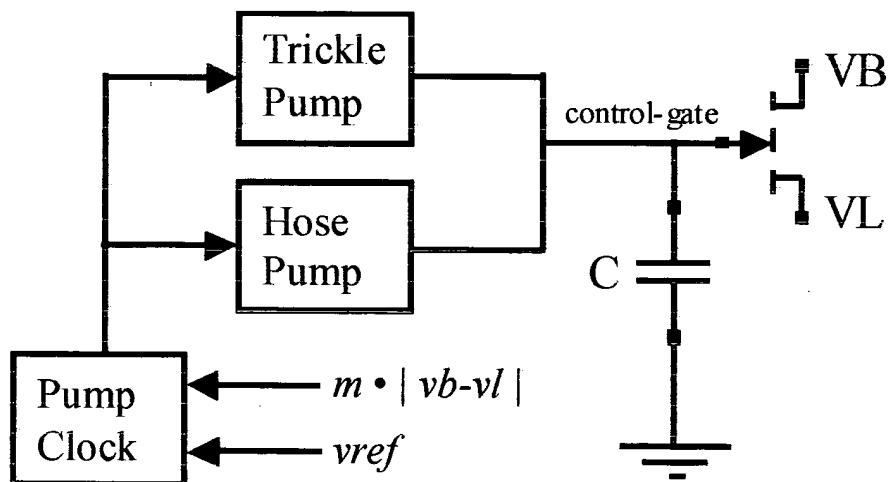


Fig. 2: An illustration of Variable-Current Drive and Dual-Function Capacitor

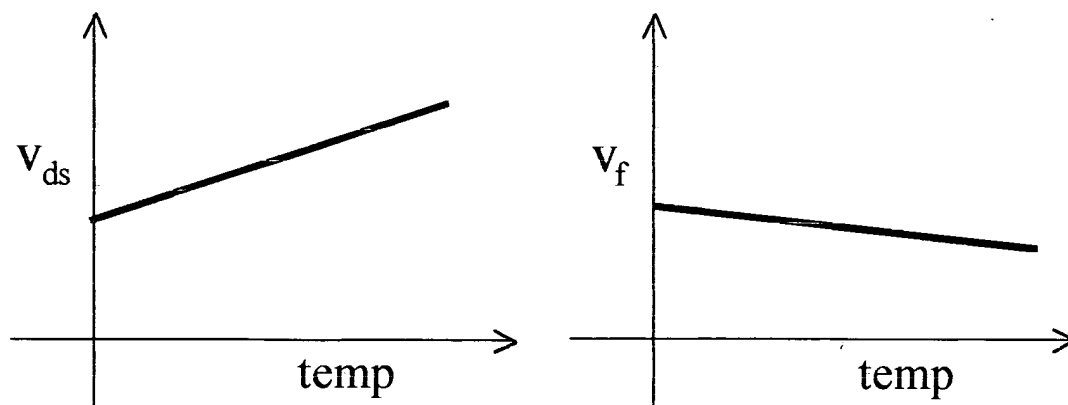


Fig. 3: Illustrations of channel and gate junction voltage drops of the switch.